

LT900 1x48 Single-Mode Fiber Optic Switch

Model Number: N148XMST-XXX2

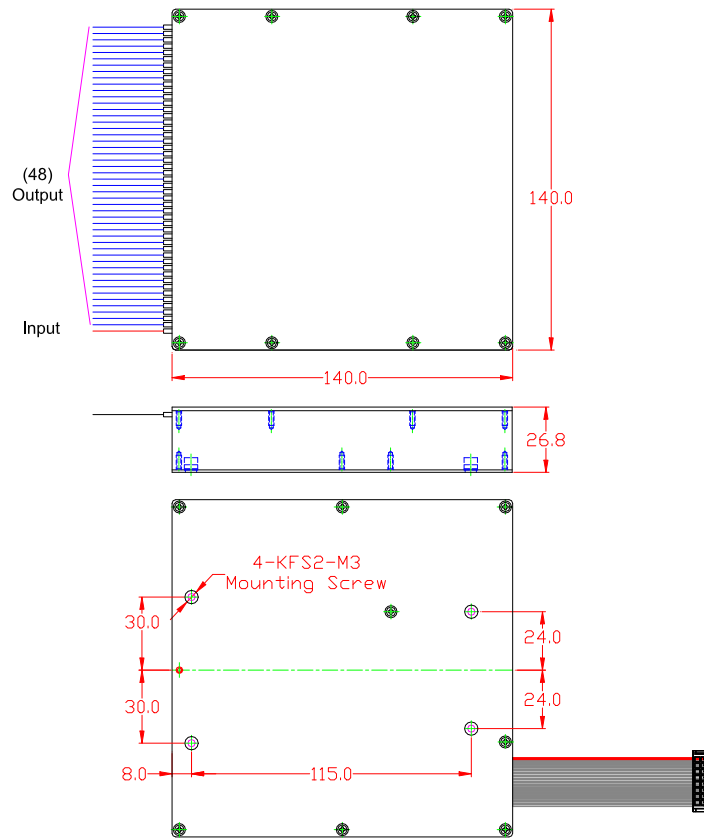
Item	Unit	Specifications
Wavelength	nm	1625
Insertion Loss	dB	< 0.8 (without connectors)
Back Reflection	dB	< - 55
Crosstalk	dB	< - 70
Polarization Dependent Loss	dB	< 0.08
Repeatability	dB	< ± 0.02
Switching Time	ms	< 65 + 10 per channel
Durability	cycle	> 10 million
Input Optical Power	mW	< 300
Control	--	16-pin TTL
Operating Voltage	V DC	5.0 \pm 0.25
Operating Temperature	$^{\circ}$ C	0 to 50 $^{\circ}$ C
Storage Temperature	$^{\circ}$ C	- 40 to 70 $^{\circ}$ C
Humidity	--	< 95% RH non-condensing
Fiber Type	--	Corning SMF-28e+ (9/125 μ m)
Pigtail Sleeve Type	--	Φ 900 μ m loose tube
Pigtail Length	m	1.0 \pm 0.1
Connector	--	SC, FC, LC or Others upon request
Housing Dimensions (H x W x L)	mm	27 x 140 x 140
Weight	g	< 600
Power Consumption	W	< 3.5

*All specifications are referenced without connectors unless noted.

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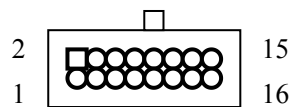
Operation Manual: LT900 1x48 Fiber Optic Switch

Dimensions:



Control Interface – 16-pin TTL

A 16-pin, 0.1" spacing dual row connector serves as the interface to all the electrical connections to the LT900 switch. The connector is attached to the switch through a 16 wire ribbon cable. There are seven pins to indicate the channel address (D0-D6), two input control pins (STROBE, RESET), two output status pins (BUSY, ERROR), two digital power pins (D5V, DGND), and two analog power pins (A5V, AGND). All I/O pins are ESD protected.



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Pin Assignments

Pin #	Signal	Type	Definition
1	A5V	Power	Analog +5V
2	AGND	Power	Analog Ground
3	DGND	Power	Digital Ground
4	D5V	Power	Digital +5V
5	STROBE	Input	High to low pulse – read channel address and reset pins High – ignore state of channel address and reset pins
6	BUSY	Output	Low – idle High – switching
7	ERROR	Output	Low – normal operation High – internal error
8	RESET	Input	Low – switch to off (reset) position High – normal operation
9	D0	Input	Channel address bit 0 (LSB)
10	D1	Input	Channel address bit 1
11	D2	Input	Channel address bit 2
12	D3	Input	Channel address bit 3
13	D4	Input	Channel address bit 4
14	D5	Input	Channel address bit 5
15	D6	Input	Channel address bit 6 (MSB)
16		-----	Reserved

Note:

1. All TTL inputs and outputs are pulled high to 5 VDC through 10K ohm resistors.
2. Low = 0V, High = +5V